REMARKS

Claim 1 calls for transferring data from a host memory to an Ethernet device.

Apparently, there is an attempt to read the claim on the cited Davis reference and, particularly, the PCI configuration cycle. However, in the PCI configuration cycle discussed in the cited portions of the text and in Figure 7, the host memory does not transfer any data. Instead, the host processor sends configuration commands (block 217). Those commands get converted (block 219), a PCI device is selected, and configuration is implemented under certain circumstances. But there is no transfer of data, there is no transfer of data from host memory to an Ethernet device, and, therefore, it is not seen how the cited reference is applicable to claim 1 or any of the other claims.

In fact, there is no discussion in the cited reference of an Ethernet device whatsoever. In addition, there is no discussion of any embedded memory of an Ethernet device.

Therefore, reconsideration is respectfully requested.

Respectfully submitted,

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